

**AUTOMATED ANALYSIS OF INTERFACE TIMING MEASUREMENTS****Abstract of the Disclosure**

A system for evaluating whether an interface between a host device and a target device complies with specifications of an industry standard, such as, without limitation, SCSI, Serial ATA, Parallel ATA and Fibre Channel Arbitrated Loop, is disclosed. The system scans a communication trace between the host device and the target device to detect a timing measure present in the communication trace. The timing measure begins with a start condition and terminates with an ending condition. The start and ending conditions may be functions of logic transitions on either multiple or single signal lines in the communication trace. After a timing measure is detected, the system evaluates the length of the timing measure against a timing measure protocol specified by the industry standard. A computer-readable program storage device which tangibly embodies a program of instructions executable by a computer system for evaluating whether the interface complies with the industry standard is also disclosed.

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